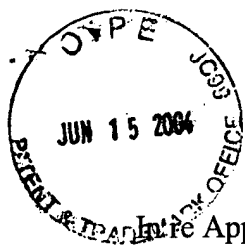


IFW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

GERT BURBACH  
MANFRED HORSTMANN  
THOMAS FEUDEL

Examiner: Hoai V. Pham

Group Art Unit: 2814

Att'y Docket: 2000.108500/DE0355

Customer No.: 23720

Serial No.: 10/629,436

Filed: July 29, 2003

For: METHOD OF PLATING A  
SEMICONDUCTOR STRUCTURE

**RESPONSE TO RESTRICTION  
REQUIREMENT DATED MAY 26, 2004**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, this 8<sup>th</sup> day of June, 2004.

*Mary Paul*  
Signature

This paper is submitted in response to the Restriction Requirement dated May 26, 2004, for which the one-month date for response is June 26, 2004.

In response to the restriction requirement which the Examiner imposed, Applicants elect, with traverse, to prosecute claims 11-24, *i.e.*, the Group II claims.

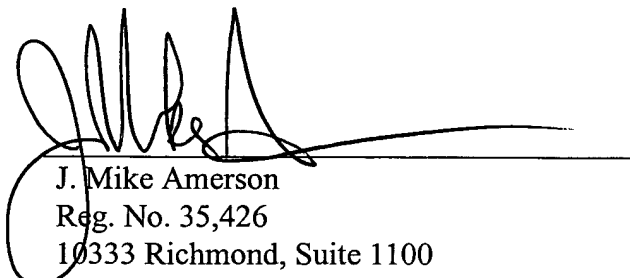
It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Director is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0355. In the event the monies in that account are insufficient, the Director is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.108500.

The Examiner is invited to contact the undersigned attorney at (713) 934-4055 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON  
CUSTOMER NO. 23720

Date: June 8, 2004



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